

| | Type | L # | Hits | Search Text | DBs | Time Stamp | Er ro r Co r re c ti on | Er ro r Co r re c ti on |
|---|------|-----|------|---|--|----------------------|---|---|
| 1 | BRS | L4 | 225 | (gate with trench) and (gate with recessed) | USPAT; EPO; JPO; DERWENT; IBM TDB | 2001/07/ 03 15:08 | | 0 |
| 2 | BRS | L15 | 31 | (gate with trench) and (input with capacitance) | USPAT; EPO; JPO; DERWENT; IBM TDB | 2001/07/ 03 15:09 | | 0 |
| 3 | BRS | L23 | 156 | (source with gate with overlap\$5) and (gate with trench) | USPAT; EPO; JPO; DERWENT; IBM TDB | 2001/07/ 03 15:30 | | 0 |
| 4 | BRS | L24 | 139 | (source with gate with capacitance\$5) and (gate with trench) | USPAT; EPO; JPO; DERWENT; IBM TDB | 2001/07/ 03 15:32 | | 0 |
| 5 | BRS | L25 | 640 | (source with sidewall\$1) and (gate with trench) | USPAT; EPO; JPO; DERWENT; IBM TDB | 2001/07/ 03 15:32 | | 0 |
| 6 | BRS | L26 | 444 | (source with trench with sidewall\$1) and (gate with trench) | USPAT; EPO; JPO; DERWENT; IBM TDB | 2001/07/ 03 15:33 | | 0 |
| 7 | BRS | L27 | 97 | (source with implant\$5 with trench with sidewall\$1) and (gate with trench) | USPAT; EPO; JPO; DERWENT; IBM TDB | 2001/07/ 03 15:33 | | 0 |